



Attorney Docket No. QCS-001DV3 (6695/2) **PATENT**

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Kamieniecki et al.

SERIAL NUMBER: 09/932,754

ART UNIT: 2829

FILING DATE: August 17, 2001

EXAMINER: J. Hollington

TITLE: Real-Time In-Line Testing Of Semiconductor Wafers

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**RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP NO. 2829**

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AMENDMENT and RESPONSE

Sir:

This paper is submitted in response to the final Office Action mailed from the Patent Office on October 28, 2002. A one-month extension of time is respectfully requested. A petition for the extension of time and a check for \$55.00 is enclosed with this response. Applicants respectfully request entry of this amendment and response, reconsideration, and passage of this application to allowance. In the event that any additional fees are due, the Commissioner is hereby authorized to charge any such fees to Attorney's Deposit Account No. 20-0531.

Please amend the application, without prejudice, as follows:

In the Drawings

Please replace FIG. 1 with the enclosed corrected drawing for FIG. 1. Also enclosed is a marked up version of FIG. 1 that was previously filed, which shows the changes to FIG. 1 in red ink. The marked up version of FIG. 1 shows reference numbers 28A and 28B added in red. No new matter has been added.